



PATENT  
Conf. No. 2982

IN THE UNITED STATES PATENT OFFICE

Applicant: Andy T. Nguyen  
Assignee: Xilinx, Inc.  
Title: "Counter-Based Clock Doubler Circuits and Methods"  
Serial No.: 10/719,830 Filing Date: 11/20/2003  
Examiner: Minh T. Nguyen Art Unit: 2816  
Docket No.: X-1464-2P US Conf. No.: 2982

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed November 17, 2004, Applicant submits the following amendments and remarks.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.